

# ***TH55XE BIOS Manual***

---

---

<b>BIOS Setup .....</b>	<b>1</b>
<b>1 Main Menu.....</b>	<b>3</b>
<b>2 Advanced Menu.....</b>	<b>8</b>
<b>3 PCIPnP Menu.....</b>	<b>22</b>
<b>4 Boot Menu.....</b>	<b>25</b>
<b>5 Chipset Menu.....</b>	<b>28</b>
<b>6 O.N.EMenu.....</b>	<b>33</b>
<b>7 Exit Menu.....</b>	<b>45</b>

# **TH55XE BIOS Manual**

---

---

## **BIOS Setup**

### **Introduction**

The purpose of this manual is to describe the settings in the AMI BIOS Setup program on this motherboard. The Setup program allows users to modify the basic system configuration and save these settings to CMOS RAM. The power of CMOS RAM is supplied by a battery so that it retains theSetup information when the power is turned off.

Basic Input-Output System (BIOS) determines what a computer can do without accessing programs from a disk. This system controls most of the input and output devices such as keyboard, mouse, serial ports and disk drives. BIOS activates at the first stage of the booting process, loading and executing the operating system. Some additional features, such as virus and password protection or chipset fine-tuning options are also included in BIOS.

The rest of this manual will guide you through the options and settings in BIOS Setup.

### **Plug and Play Support**

This AMI BIOS supports the Plug and Play Version 1.0A specification.

### **EPA Green PC Support**

This AMI BIOS supports Version 1.03 of the EPA Green PC specification.

### **APM Support**

This AMI BIOS supports Version 1.1&1.2 of the Advanced Power Management (APM) specification. Power management features are implemented via the System Management Interrupt (SMI). Sleep and Suspend power management modes are supported. Power to the hard disk drives and video monitors can also be managed by this AMI BIOS.

### **ACPI Support**

AMI ACPI BIOS support Version 1.0/2.0 of Advanced Configuration and Power interface specification (ACPI). It provides ASL code for power management and device configuration capabilities as defined in the ACPI specification, developed by Microsoft, Intel and Toshiba.

# **TH55XE BIOS Manual**

---

---

## **PCI Bus Support**

This AMI BIOS also supports Version 2.3 of the Intel PCI (Peripheral Component Interconnect) local bus specification.

## **DRAM Support**

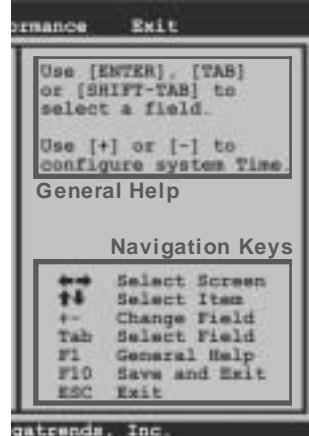
DDR3 SDRAM (Double Data Rate III Synchronous DRAM) is supported.

## **Supported CPUs**

This AMI BIOS supports the Intel CPU.

## **Using Setup**

When starting up the computer, press **<Del>** during the **Power-On Self-Test (POST)** to enter the BIOS setup utility. In the BIOS setup utility, you will see **General Help** description at the top right corner, and this is providing a brief description of the selected item. **Navigation Keys** for that particular menu are at the bottom right corner, and you can use these keys to select item and change the settings.



## **Notice**

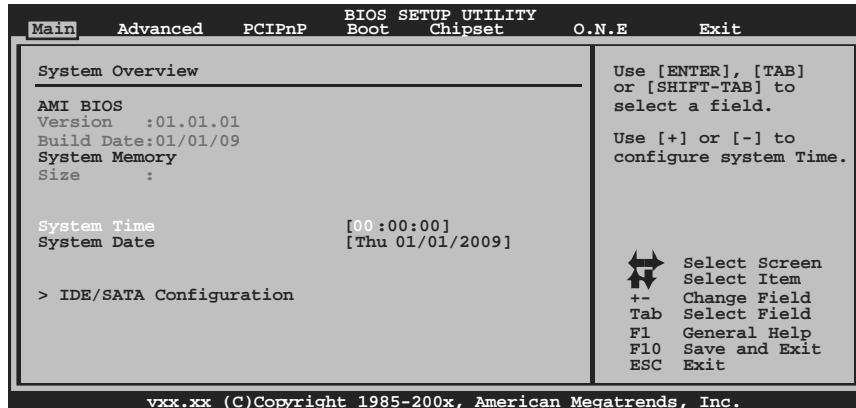
- The default BIOS settings apply for most conditions to ensure optimum performance of the motherboard. If the system becomes unstable after changing any settings, please load the default settings to ensure system's compatibility and stability. Use Load Setup Default under the Exit Menu.
- For better system performance, the BIOS firmware is being continuously updated. The BIOS information described in this manual is for your reference only. The actual BIOS information and settings on board may be slightly different from this manual.
- The content of this manual is subject to be changed without notice. We will not be responsible for any mistakes found in this user's manual and any system damage that may be caused by wrong-settings.

# ***TH55XE BIOS Manual***

---

## **1 Main Menu**

Once you enter AMI BIOS Setup Utility, the Main Menu will appear on the screen providing an overview of the basic system information.



### **AMI BIOS**

Shows system information including BIOS version, built date, etc.

### **System Memory**

Shows system memory size, VGA shard memory will be excluded.

### **System Time**

Set the system internal clock.

### **System Date**

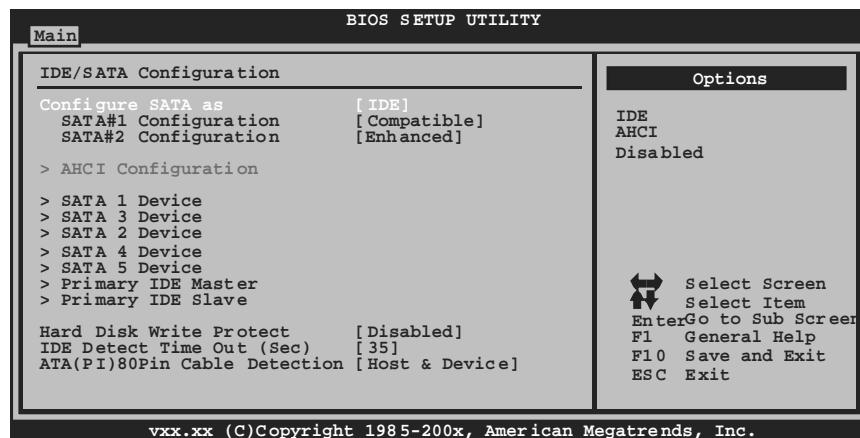
Set the system date. Note that the ‘Day’ automatically changes when you set the date.

# **TH55XE BIOS Manual**

---

## **IDE/SATA Configuration**

The BIOS will automatically detect the presence of IDE/SATA devices. There is a sub-menu for each IDE/SATA device. Select a device and press <Enter> to enter the sub-menu of detailed options.



### **Configure SATA as**

This item allows you to choose the SATA operation mode.

Options: IDE (Default) / AHCI / Disabled

### **SATA#1 Configuration**

This item allows you to control the onboard SATA controller.

Options: Compatible (Default) / Enhanced

### **SATA#2 Configuration**

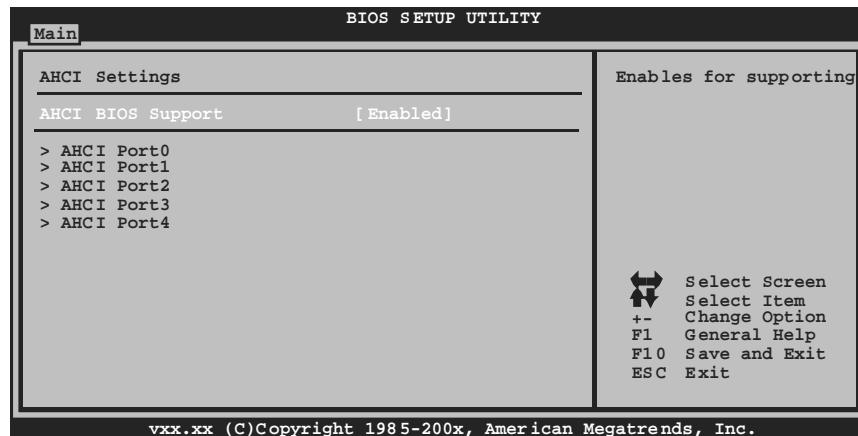
This item allows you to control the onboard SATA controller.

Options: Enhanced (Default) / Disabled

# TH55XE BIOS Manual

---

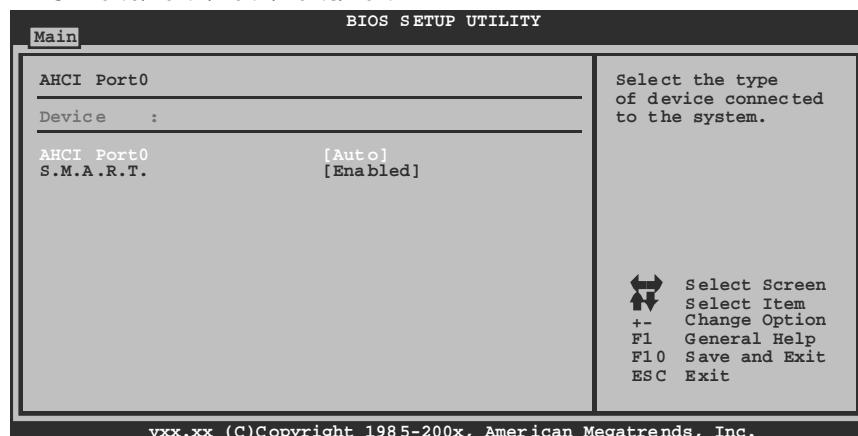
## AHCI Configuration



### AHCI BIOS Support

This BIOS feature controls the AHCI function of the SATA controller.  
Options: Enabled (Default) / Disabled

### AHCI Port0/Port1/Port2/Port3/Port4



# TH55XE BIOS Manual

---

## ***Device***

This area shows the detected connected device.

## ***SATA Port0/1/2/3/4***

This item allows you to select the connected device type.

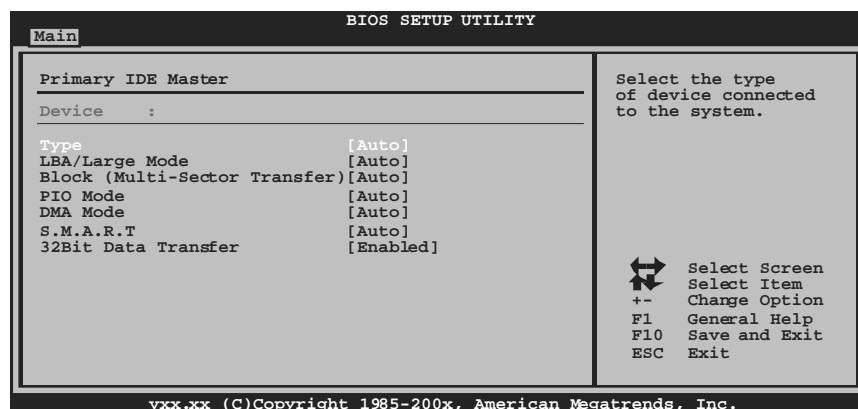
Options: Auto (Default) / Not Installed

## ***S.M.A.R.T.***

This item allows you to control the device S.M.A.R.T function.

Options: Enabled (Default) / Disabled

## **SATA 1/3/2/4/5 Device; Primary IDE Master/Slave**



The BIOS detects the information and values of respective devices, and these information and values are shown below to the name of the sub-menu.

## ***Type***

Select the type of the IDE/SATA drive.

Options: Auto (Default) / CDROM / ARMD / Not Installed

## ***LBA/Large Mode***

Enable or disable the LBA mode.

Options: Auto (Default) / Disabled

## ***Block (MultiSector Transfer)***

Enable or disable multi-sector transfer.

Options: Auto (Default) / Disabled

# **TH55XE BIOS Manual**

---

## ***PIO Mode***

Select the PIO mode.

Options: Auto (Default) / 0 / 1 / 2 / 3 / 4

## ***DMA Mode***

Select the DMA mode.

Options: Auto (Default) / SWDMA0 ~ 2 / MWDMA0 ~ 2 / UDMA0 ~ 5

## ***S.M.A.R.T***

Set the Smart Monitoring, Analysis, and ReportingTechnology.

Options: Auto (Default) / Disabled / Enabled

## ***32Bit Data Transfer***

Enable or disable 32-bit data transfer.

Options: Enabled (Default) / Disabled

## ***Hard Disk Write Protect***

Disable or enable device write protection. This will be effective only if the device is accessed through BIOS.

Options: Disabled (Default) / Enabled

## ***IDE Detect Time Out (Sec)***

Select the time out value for detecting IDE/SATA devices.

Options: 35 (Default) / 30 / 25 / 20 / 15 / 10 / 5 / 0

## ***ATA(PI) 80Pin Cable Detection***

Select the mechanism for detecting 80Pin AT A(PI) Cable..

Options: Host & Device (Default) / Host / Device

# TH55XE BIOS Manual

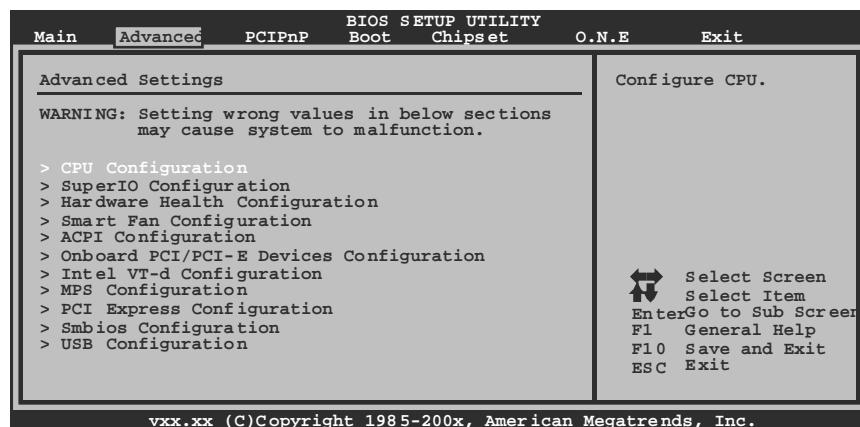
---

## 2 Advanced Menu

The Advanced Menu allows you to configure the settings of CPU, Super I/O, Power Management, and other system devices.

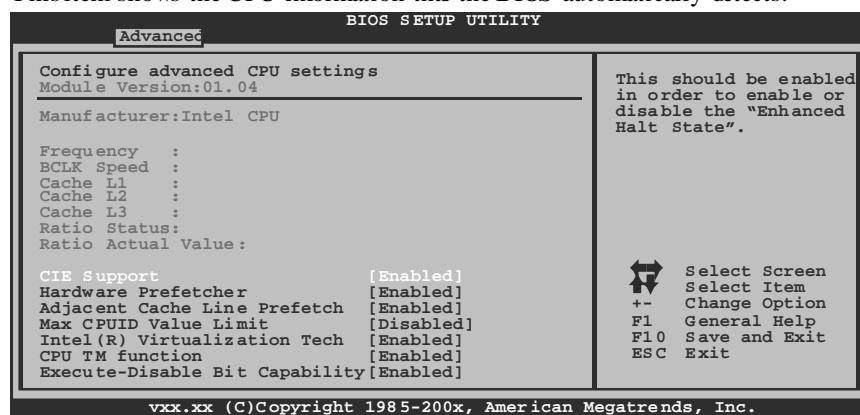
### Notice

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



### CPU Configuration

This item shows the CPU information that the BIOS automatically detects.



# **TH55XE BIOS Manual**

---

---

## **C1E Support**

C1E is “Enhanced Halt State” function, this function helps to save power and decrease heat by lowering CPU frequency while the processor is not working.

Options: Enabled (Default) / Disabled

## **Hardware Prefetcher**

The processor has a hardware prefetcher that automatically analyzes its requirements and prefetches data and instructions from the memory into the Level 2 cache that are likely to be required in the near future. This reduces the latency associated with memory reads.

Options: Enabled (Default) / Disabled

## **Adjacent Cache Line Prefetch**

The processor has a hardware adjacent cache line prefetch mechanism that automatically fetches an extra 64-byte cache line whenever the processor requests for a 64-byte cache line. This reduces cache latency by making the next cache line immediately available if the processor requires it as well.

Options: Enabled (Default) / Disabled

## **Max CPUID Value Limit**

When the computer is booted up, the operating system executes the CPUID instruction to identify the processor and its capabilities. Before it can do so, it must first query the processor to find out the highest input value CPUID recognizes. This determines the kind of basic information CPUID can provide the operating system.

Options: Disabled (Default) / Enabled

## **Intel(R) Virtualization Tech**

Virtualization Technology can virtually separate your system resource into several parts, thus enhance the performance when running virtual machines or multi interface systems.

Options: Enabled (Default) / Disabled

## **CPU TM Function**

The CPU TM Function is to throttle the clock speed of higher speed Prescott's to help keep them cool.

Options: Disabled (Default) / Enabled

# **TH55XE BIOS Manual**

---

---

## **Execute-Disable Bit Capability**

This item allows you to configure the Execute Disabled Bit function, which protects your system from buffer overflow attacks.

Options: Enabled (Default) / Disabled

## **Intel (R) HT Technology**

Hyper Threading Technology can improve performance by splitting instructions into multiple streams.

Options: Enabled (Default) / Disabled

## **Active Processor Cores**

This item allows you to set the number of cores to enable in each processor package.

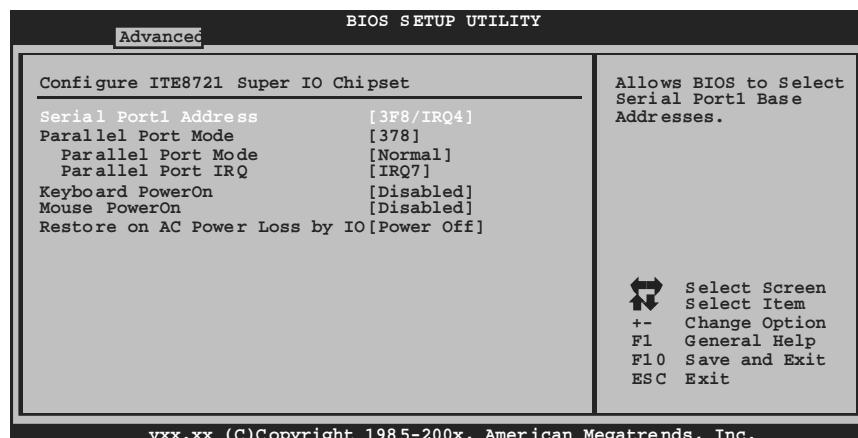
Options: All (Default) / 1 / 2

## **A20M**

Legacy OSes and APs may need A20M enabled.

Options: Disabled (Default) / Enabled

## **SuperIO Configuration**



# **TH55XE BIOS Manual**

---

---

## **Serial Port1 Address**

Select an address and corresponding interrupt for the first and second serial ports.  
Options: 3F8/IRQ4 (Default) / 2F8/IRQ3 / 3E8/IRQ4 / 2E8/IRQ3 / Auto / Disabled

## **Parallel Port Address**

This item allows you to determine access onboard parallel port controller with which I/O Address.  
Options: 378 (Default) / 278 / 3BC / Disabled

## **Parallel Port Mode**

This item allows you to determine how the parallel port should function.  
Options: Normal (Default) Using Parallel port as Standard Printer Port.  
EPP Using Parallel Port as Enhanced Parallel Port.  
ECP Using Parallel port as Extended Capabilities Port.  
ECP+EPP Using Parallel port as ECP & EPP mode

## **ECP Mode DMA Channel**

This item allows you to select parallel port ECP DMA.  
Options: DMA3 (Default) / DMA0 / DMA1

## **Parallel Port IRQ**

This item allows you to select the IRQ for the onboard parallel port.  
Options: IRQ7 (Default) / IRQ5 / Disabled

## **Keyboard PowerOn**

This item allows you to control the keyboard power on function.  
Options: Disabled (Default) / Specific Key / Stroke Key

## **Specific Key Enter**

This item will show only when Keyboard PowerOn is set "Specific Key."

# **TH55XE BIOS Manual**

---

## **Stroke Keys Selected**

This item will show only when Keyboard PowerOn is set “Stroke Key.”

Options: Ctrl+F1 (Default) / Wake Key / Power Key / Ctrl+F2 / Ctrl+F3 /  
Ctrl +F4 / Ctrl+F5 / Ctrl+F6

## **Mouse PowerOn**

This item allows you to control the mouse power on function.

Options: Disabled (Default) / Enabled

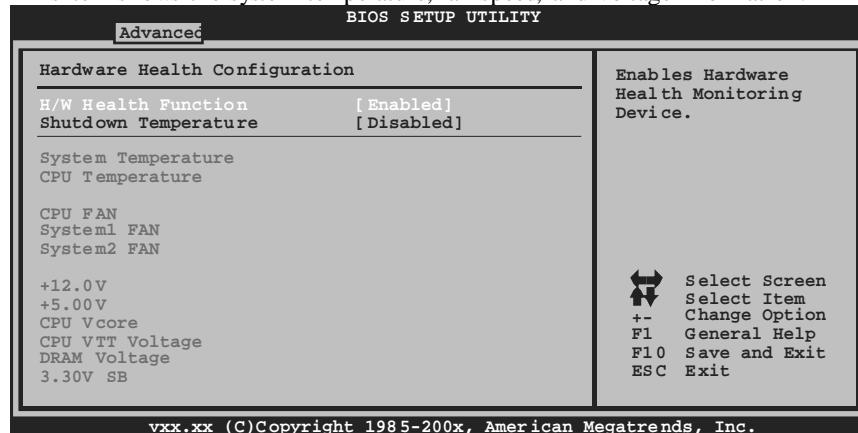
## **Restore on AC Power Loss by IO**

This setting specifies how your system should behave after a power fail or interrupts occurs. By choosing Disabled will leave the computer in the power off state. Choosing Enabled will restore the system to the status before power failure or interrupt occurs.

Options: Power Off (Default) / Power On / Last State

## **Hardware Health Configuration**

This item shows the system temperature, fan speed, and voltage information.



## **H/W Health Function**

If with a monitoring system, the system will show PC health status during POST stage.

Options: Enabled (Default) / Disabled

# **TH55XE BIOS Manual**

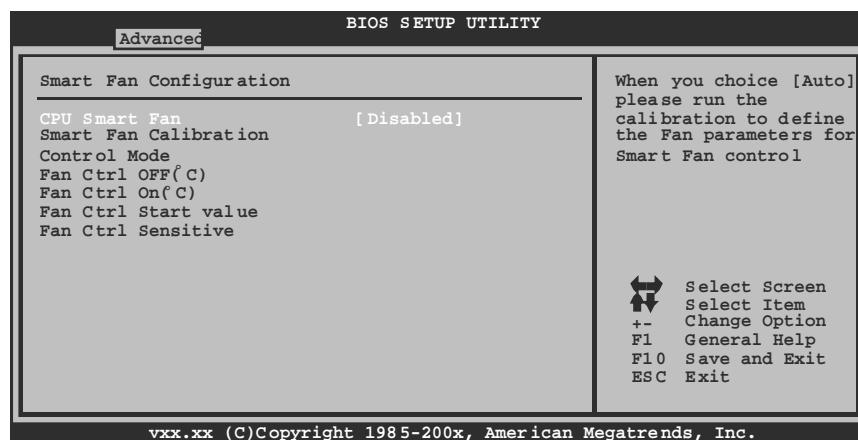
---

## **Shutdown Temperature**

This item allows you to set up the CPU shutdown Temperature. This item is only effective under Windows 98 ACPI mode.

Options: Disabled (Default) / 60°C/140°F / 65°C/149°F / 70°C/158°F / 75°C/167°F  
/ 80°C/176°F / 85°C/185°F / 90°C/194°F

## **Smart Fan Configuration**



### **CPU Smart Fan**

This item allows you to control the CPUSmart Fan function.  
Options: Disabled (default) / Auto

### **Smart Fan Calibration**

Choose this item and then the BIOS will auto test and detect the CPU/System fan functions and show CPU/System fan speed.

### **Control Mode**

This item provides several operation modes of the fan.  
Options: Quiet / Performance / Manual

### **Fan Ctrl OFF(°C)**

If the CPU/System Temperature is lower than the set value, FAN will turn off.  
Options: 0~127 (°C) (Interval: 1°C)

# **TH55XE BIOS Manual**

---

## **Fan Ctrl On(°C )**

CPU/System fan starts to work under smart fan function when arrive this set value.  
Options: 0~127 (°C) (Interval: 1°C)

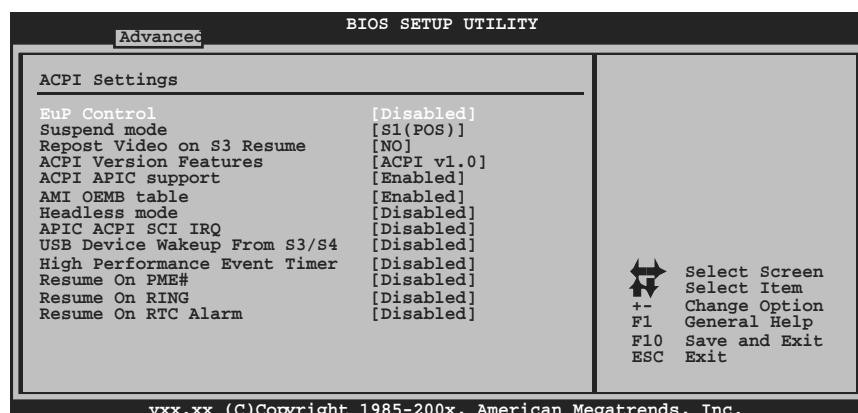
## **Fan Ctrl Start Value**

When CPU/System temperature arrives to the set value, the CPU/System fan will work under Smart Fan Function mode.  
Options: 0~127 (Interval: 1)

## **Fan Ctrl Sensitive**

Increasing the value will raise the speed of CPU/System fan.  
Options: 1~127 (Interval: 1)

## **ACPI Configuration**



## **EuP Control**

This item is used to enable or disable EuP Control ((Energy Using Products)).  
Options: Disabled (Default) / Enabled

# **TH55XE BIOS Manual**

---

## **Suspend mode**

The item allows you to select the suspend type under the ACPI operating system.

Options: S1 (POS) (Default)      Power on Suspend  
          S3 (STR)                  Suspend to RAM  
          Auto                        POS+STR

## **Repost Video on S3 Resume**

The item allows you to determine whether to invoke VGA BIOS post on S3/STR resume.

Options: No (Default) / Yes

## **ACPI Version Features**

The item allows you to select the version of ACPI

Options: ACPI v1.0 (Default) / ACPI v2.0 / ACPI v3.0

## **ACPI APIC support**

This item is used to enable or disable the motherboard's APIC (Advanced Programmable Interrupt Controller). The APIC provides multiprocessor support, more IRQs and faster interrupt handling.

Options: Enabled (Default) / Disabled

## **AMI OEMB table**

Set this value to allow the ACPI BIOS to add a pointer to an OEMB table in the Root System Description Table (RSDT) table.

Options: Enabled (Default) / Disabled

## **Headless mode**

This is a server-specific feature. A headless server is one that operates without a keyboard, monitor or mouse. To run in headless mode, both BIOS and operating system (e.g. Windows Server 2003) must support headless operation.

Options: Disabled (Default) / Enabled

## **APIC ACPI SCI IRQ**

Options: Disabled (Default) / Enabled

# **TH55XE BIOS Manual**

---

---

## **USB Device Wakeup from S3/S4**

This item allows you to enable or disabled the USB resume from S3/S4 function.  
Options: Disabled (Default) / Enabled

## **High Performance Event Timer**

This item allows you to enable or disabled the HPET.  
Options: Disabled (Default) / Enabled

## **Resume On PME#**

When you select Enabled, a PME signal from PCI card returns the system to Full ON state.

For this function to work, you may need a LAN add-on card which supports the Wake on LAN function. Set the Wake on LAN (WOL) jumper on motherboard to enable if applicable.

Options: Disabled (Default) / Enabled

## **Resume on RING**

This item allows you control the wake on ring function.  
Options: Disabled (Default) / Enabled

## **Resume On RTC Alarm**

When “Enabled”, you can set the date and time at which the RTC (real-time clock) alarm awakens the system from Suspend mode.

Options: Disabled (Default) / Enabled

## **RTC Alarm Date (Days)**

You can choose which date the system will boot up.

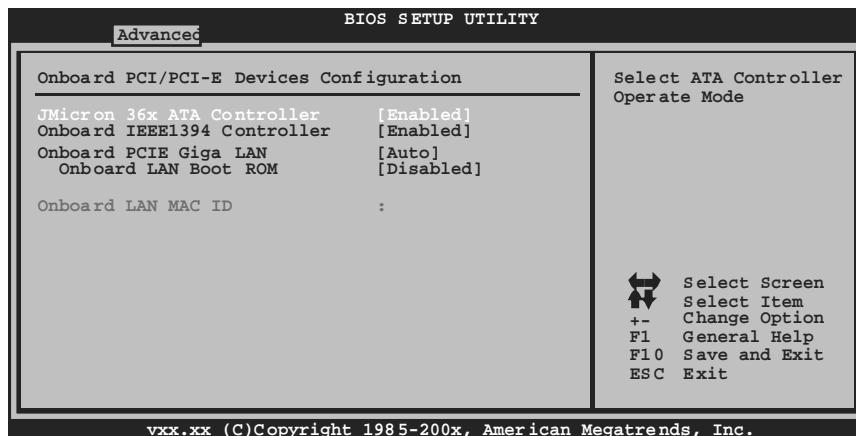
## **System Time**

You can choose the system boot up time, input hour, minute and second to specify.

# **TH55XE BIOS Manual**

---

## **Onboard PCI/PCI-E Devices Configuration**



### **JMicron 36x ATA Controller**

This item allows you to select ATA controller operate mode.

Options: Enabled (Default) / Disabled

### **Onboard IEEE 1394 Controller**

This item allows you to activate IEEE 1394 Controller.

Options: Enabled (Default) / Disabled

### **Onboard PCIE Giga LAN**

This item allows you to control the onboard LAN.

Options: Auto (Default) / Enabled / Disabled

### **Onboard LAN Boot Rom**

This item allows you to select the Onboard LAN Boot ROM.

Options: Disabled (Default) / Enabled

### **Onboard LAN MAC ID**

This item shows the LAN MAC ID.

# ***TH55XE BIOS Manual***

---

## **Intel VT-d Configuration**

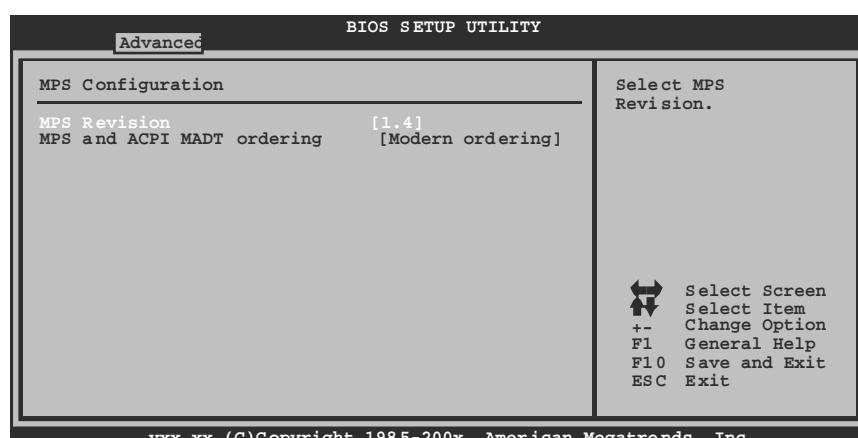


### **Intel VT-d**

Intel(R) Virtualization Technology for Directed I/O (VT-d) provides hardware assists for virtualization, improving security, reliability, and performance of I/O devices in virtualized environment.

Options: Disabled (Default) / Enabled

## **MPS Configuration**



# ***TH55XE BIOS Manual***

---

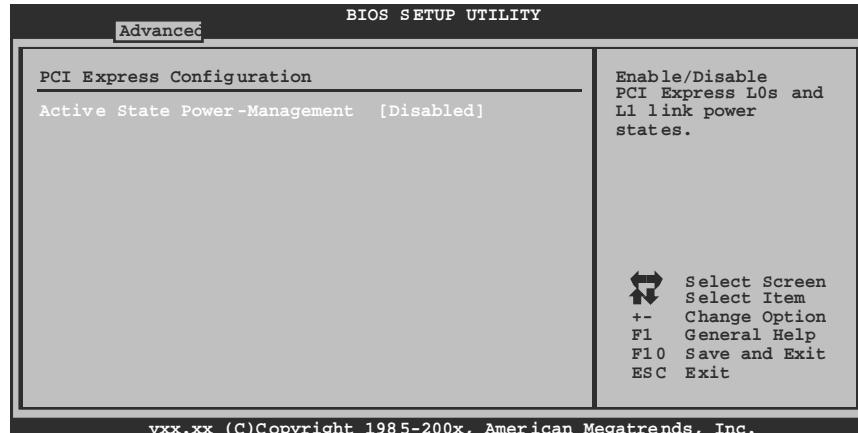
## **MPS Revision**

The BIOS supports version 1.1 and 1.4 of the Intel multiprocessor specification.  
Select version supported by the operating system running on this computer.  
Option: 1.4 (Default) / 1.1

## **MPS and ACPI MADT ordering**

Modern ordering is for Windows XP or later OSes. Legacy ordering is for Windows 2000 or earlier OSes.  
Option: Modern ordering (Default) / Legacy ordering

## **PCI Express Configuration**



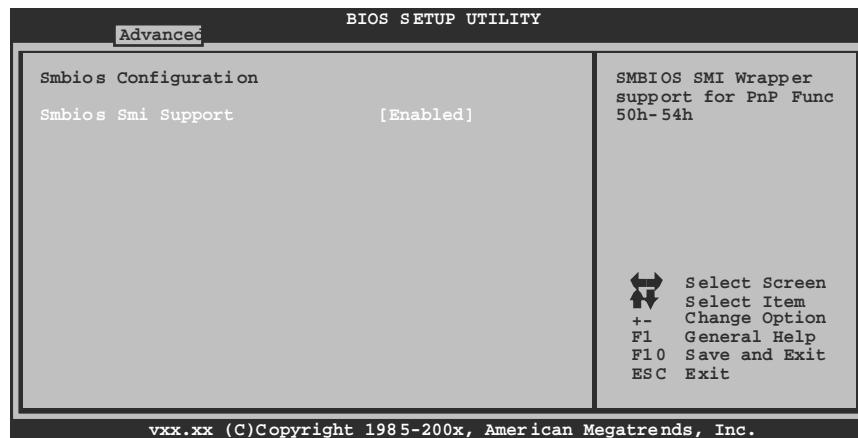
### **Active State Power-Management**

Enable or disable PCI Express L0s and L1 link power states.  
Option: Disabled (Default) / Enabled

# ***TH55XE BIOS Manual***

---

## **Smbios Configuration**

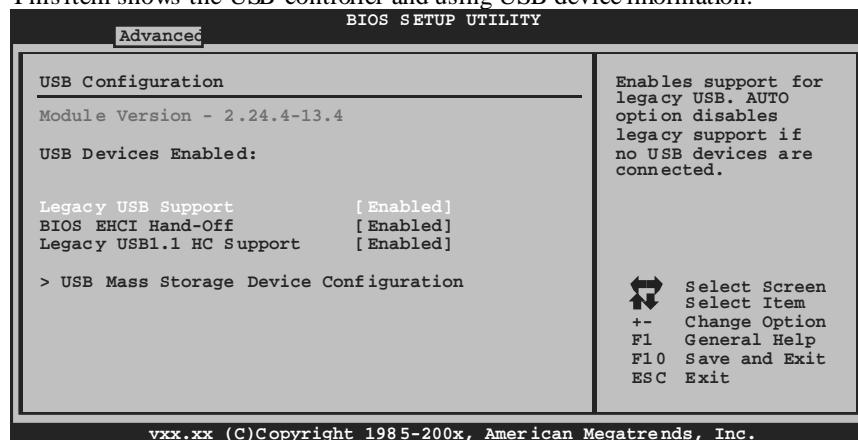


## **Smbios Configuration**

SMBIOS SMIWrapper support for PnP func 50h-54h.  
Option: Enabled (Default) / Disabled

## **USB Configuration**

This item shows the USB controller and using USB device information.



# TH55XE BIOS Manual

---

## Legacy USB Support

This item determines if the BIOS should provide legacy support for USB devices like the keyboard, mouse, and USB drive. This is a useful feature when using such USB devices with operating systems that do not natively support USB (e.g. Microsoft DOS or Windows NT).

Options: Enabled (Default) / Disabled

## BIOS EHCI Hand-Off

This item allows you to enable support for operating systems without an EHCI hand-off feature.

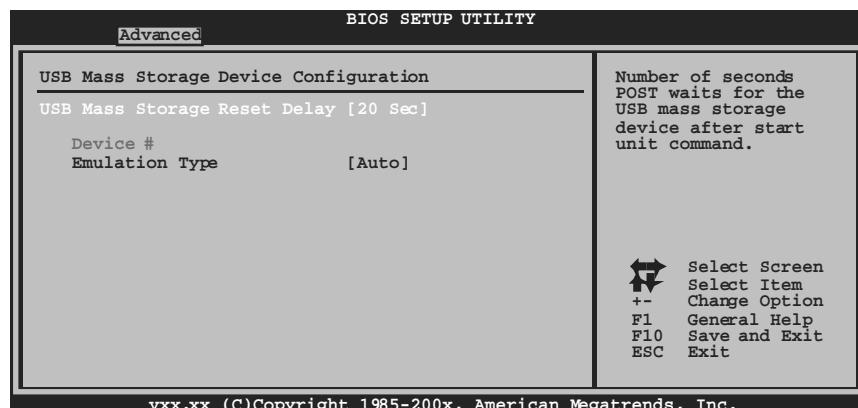
Options: Enabled (Default) / Disabled

## Legacy USB1.1 HC Support

This item allows you to activate USB 1.1 HC support.

Options: Enabled (Default) / Disabled

## USB Mass Storage Device Configuration



### USB Mass Storage Reset Delay

This item allows you to set the reset delay for USB mass storage device.

Options: 20 Sec (Default) / 10 Sec / 30 Sec / 40 Sec

### Emulation Type

This item allows you to select the emulation type of the USB mass storage device.

Options: Auto (Default) / Floppy / Forced FDD / Hard Disk / CDROM

# **TH55XE BIOS Manual**

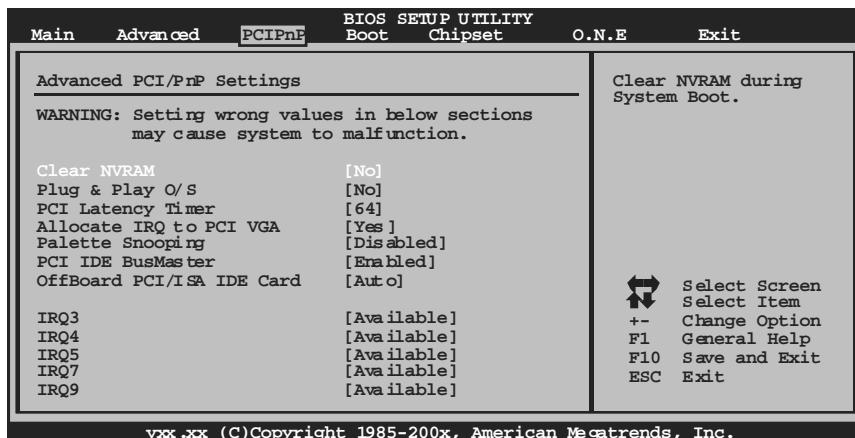
---

## **3 PCIPnP Menu**

This section describes configuring the PCI bus system. PCI, or Personal Computer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed of the CPU itself when communicating with its own special components.

### **Notice**

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



### **Clear NVRAM**

This item allows you to clear the data in the NVRAM (CMOS) by selecting “Yes”.  
Options: No (Default) / Yes

### **Plug & Play OS**

When set to YES, BIOS will only initialize the PnP cards used for the boot sequence (VGA, IDE, SCSI). The rest of the cards will be initialized by the PnP operating system like Window™ 95. When set to NO, BIOS will initialize all the PnP cards. For non-PnP operating systems (DOS, Netware™), this option must set to NO.

Options: No (Default) / Yes

# ***TH55XE BIOS Manual***

---

---

## **PCI Latency Timer**

This item controls how long a PCI device can hold the PCI bus before another takes over. The longer the latency, the longer the PCI device can retain control of the bus before handing it over to another PCI device.

Options: 64 (Default) / 32 / 96 / 128 / 160 / 192 / 224 / 248

## **Allocate IRQ to PCI VGA**

This item allows BIOS to choose a IRQ to assign for the PCI VGA card.

Options: Yes (Default) / No

## **Palette Snooping**

Some old graphic controllers need to “snoop” on the VGA palette and then map it to their display as a way to provide boot information and VGA compatibility. This item allows such snooping to take place.

Options: Disabled (Default) / Enabled

## **PCI IDE BusMaster**

This item is a toggle for the built-in driver that allows the onboard IDE controller to perform DMA (Direct Memory Access) transfers.

Options: Enabled (Default) / Disabled

## **OffBoard PCI/ISA IDE Card**

Some PCI IDE cards may require this to be set to the PCI slot number that is holding the card.

Options: Auto (Default) / PCI Slot1 ~ 6

## **OffBoard PCI/ISA Primary & Secondary IRQ**

This item allows you to set IRQ of non-onboard PCI/ISA IDE controller adapter.

Options: Disabled (Default) / INTA / INTB / INTC / INTD / Hardwired

## **IRQ3/4/5/7/9/10/11/14/15**

These items will allow you to assign each system interrupt a type, depending on the type of device using the interrupt. The option “Available” means the IRQ is going to assign automatically.

Options: Available (Default) / Reserved

# ***TH55XE BIOS Manual***

---

---

## **DMA Channel 0/1/3/5/6/7**

These items will allow you to assign each DMA channel a type, depending on the type of device using the channel. The option “Available” means the channel is going to assign automatically.

Options: Available (Default) / Reserved

## **Reserved Memory Size**

This item allows BIOS to reserve certain memory size for specific ISA device.

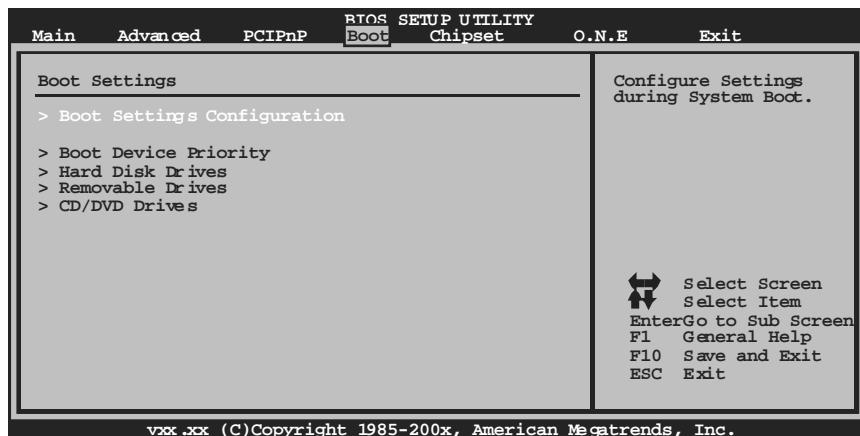
Options: Disabled (Default) / 16K / 32K / 64K

# **TH55XE BIOS Manual**

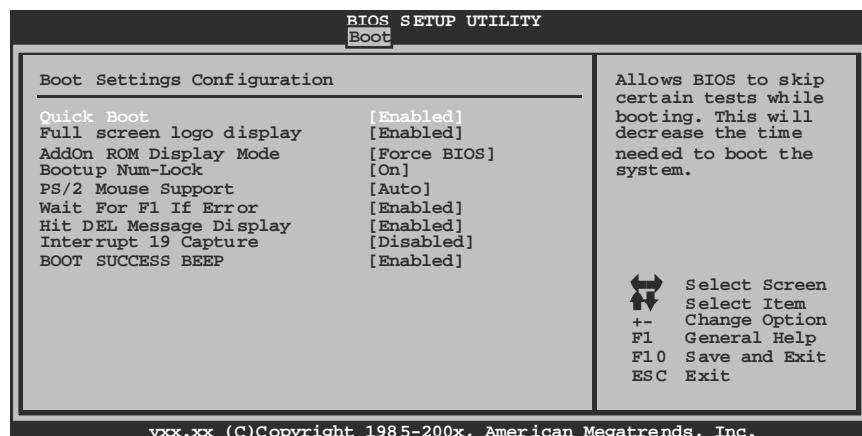
---

## **4 Boot Menu**

This menu allows you to setup the system boot options.



### **Boot Settings Configuration**



# **TH55XE BIOS Manual**

---

---

## **Quick Boot**

Enabling this option will cause an abridged version of the Power On Self-Test (POST) to execute after you power up the computer.

Options: Enabled (Default) / Disabled

## **Full Screen LOGO Display**

This item allows you to enable/disable Full Screen LOGO Show function.

Options: Enabled (Default) / Disabled

## **AddOn ROM Display Mode**

This item sets the display mode for option ROM.

Options: Force BIOS (Default) / Keep Current

## **Bootup Num-Lock**

Selects the NumLock State after the system switched on.

Options: ON (Default) / OFF

## **PS/2 Mouse Support**

This BIOS feature determines if the BIOS should reserve IRQ12 for the PS/2 mouse or allow other devices to make use of this IRQ.

Options: Auto (Default) / Disabled / Enabled

## **Wait for ‘F1’ If Error**

This BIOS feature controls the system's response when an error is detected during the boot sequence.

Options: Enabled (Default) / Disabled

## **Hit ‘DEL’ Message Display**

This BIOS feature allows you to control the display of the Hit “DEL” to enter Setup message during memory initialization.

Options: Enabled (Default) / Disabled

# **TH55XE BIOS Manual**

---

---

## **Interrupt 19 Capture**

Interrupt 19 is the software interrupt that handles the boot disk function. When set to Enabled, this item allows the option ROMs to trap interrupt 19.

Options: Disabled (Default) / Enabled

## **BOOT SUCCESS BEEP**

When this item is set to Enabled, BIOS will let user know boot success with beep.

Options: Enabled (Default) / Disabled

## **Boot Device Priority**

Items in this sub-menu specify the boot device priority sequence from the available devices. The number of device items that appears on the screen depends on the number of devices installed in the system.

## **Hard Disk Drives**

The BIOS will attempt to arrange the hard disk boot sequence automatically. You can also change the booting sequence. The number of device items that appears on the screen depends on the number of devices installed in the system.

## **Removable Drives**

The BIOS will attempt to arrange the removable drive boot sequence automatically. You can also change the booting sequence. The number of device items that appears on the screen depends on the number of devices installed in the system.

## **CD/DVD Drives**

The BIOS will attempt to arrange the CD/DVD drive boot sequence automatically. You can also change the booting sequence. The number of device items that appears on the screen depends on the number of devices installed in the system.

# **TH55XE BIOS Manual**

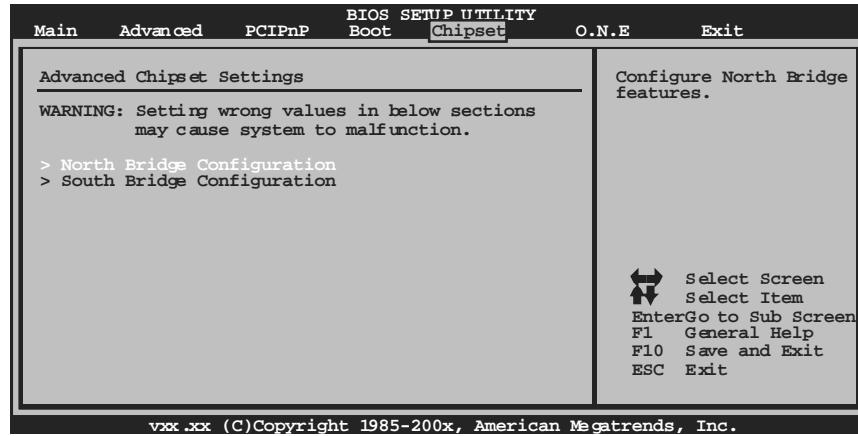
---

## **5 Chipset Menu**

This submenu allows you to configure the specific features of the chipset installed on your system. This chipset manage bus speeds and access to system memory resources, such as DRAM. It also coordinates communications with the PCI bus.

### **Notice**

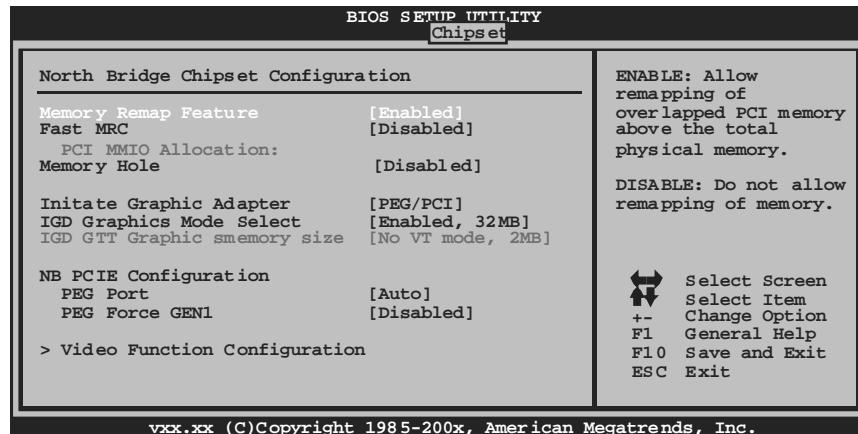
- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



# TH55XE BIOS Manual

---

## North Bridge Configuration



### **Memory Remap Feature**

This item allows you to enable or disable the remapping of the overlapped PCI memory above the total physical memory. Only 64-bit OS supports this function.  
Options: Enabled (Default) / Disabled

### **Fast MRC**

Enabled: While cold booting, MRC directly restores memory data from valid NVRAM without hardware training. Disabled: The cold booting with memory detection and hardware training.

Options: Disabled (Default) / Enabled

### **Memory Hole**

You can reserve this area of system memory for ISA adapter ROM. When this area is reserved it cannot be cached. Check the user information of peripherals that need to use this area of system memory for the memory requirements.

Options: Disabled (Default) / 15M-16M

### **Initiate Graphic Adapter**

This item allows you to select which graphics controller to use as the primary boot device.

Options: PEG/PCI (Default) / IGD / PCI/IGD / PCI/PEG / PEG/IGD

# **TH55XE BIOS Manual**

---

## **IGD Graphics Mode Select**

This item will be different as your memory modules. When the memory size is decided, this frame buffer size will also be fixed.

Options: Enabled,32MB (Default) / Enabled,64MB / Enabled,128MB / Disabled

## **PEG Port**

This BIOS feature is a toggle that enables or disables the PCI Express port.

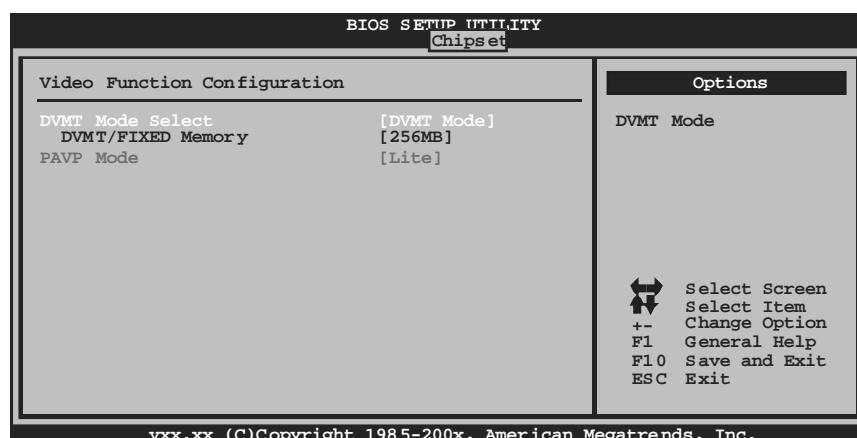
Options: Auto (Default) / Disabled

## **PEG Force GEN1**

Some non-graphics PCI-E devices may not follow PCI-E Specification and may incorrectly report their Gen capability or link width.

Options: Disabled (Default) / Enabled

## **Video Function Configuration**



### **DVMT Mode Select**

This item allows you to select the DVMT mode.

Options: DVMT Mode (Default)

# **TH55XE BIOS Manual**

---

## **DVMT/FIXED Memory Size**

DVMT stands for “Dynamic Video Memory Technology”. This is an enhancement of the unified memory architecture (UMA) concept. DVMT will set the optimum amount of memory to be allocated for a balance between graphics and system performance. DVMT dynamically respond to system requirements and applications demands, by allocating the proper amount of display, texturing and buffer memory after the operating system has booted.

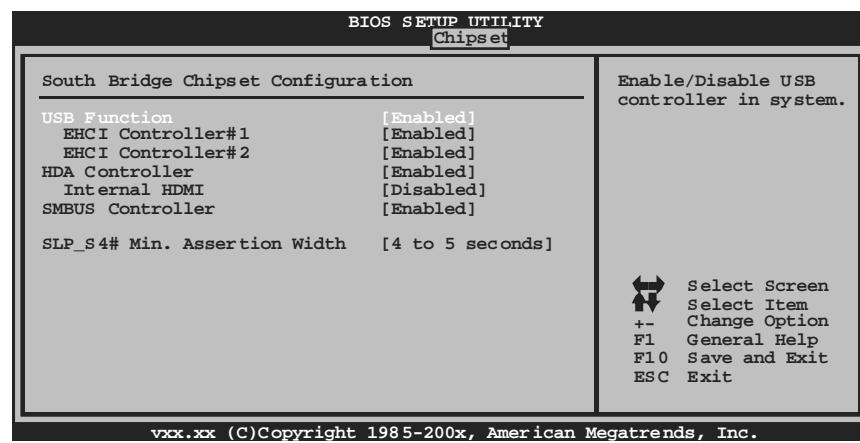
Options: 256MB (Default) / 128MB / Maximum DVMT

## **PAVP Mode**

GMCH Protected Audio Video Path (PAVP) BIOS support.

Options: Lite (Default) / Disabled / High

## **South Bridge Configuration**



### **USB Function**

This item allows you to activate USB function.

Options: Enabled (Default) / Disabled

### **EHCI Controller#1/2**

This item allows you to enable integrated USB 2.0 RMH#1/2.

Options: Enabled (Default) / Disabled

# ***TH55XE BIOS Manual***

---

---

## **HDA Controller**

This item allows you to control the HD Audio support.  
Options: Enabled (Default) / Disabled

## **Internal HDMI**

This item allows you to control enable or disable Internal HDMI.  
Options: Disabled (Default) / Enabled

## **SMBUS Controller**

This BIOS feature controls the I/O buffers for the SMBus.  
Options: Enabled (Default) / Disabled

## **SLP\_S4# Min. Assertion Width**

Options: 4 to 5 seconds (Default) / 3 to 4 seconds / 24 to 3 seconds / 1 to 2 seconds

# **TH55XE BIOS Manual**

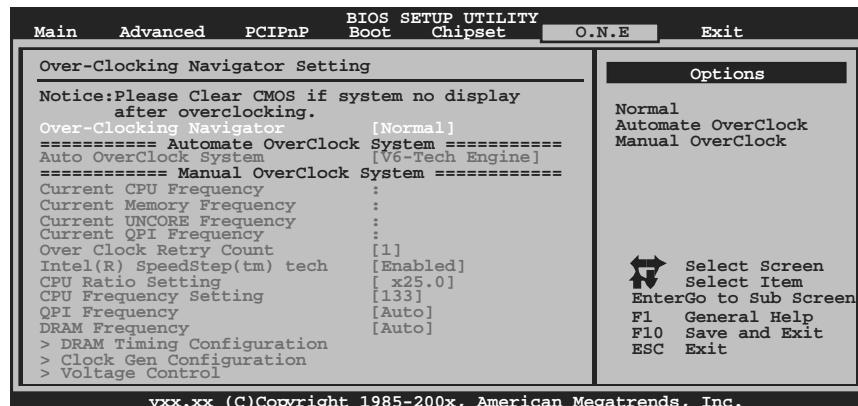
---

## **6 O.N.E Menu**

This submenu allows you to change voltage and clock of various devices.  
(However, we suggest you use the default setting. Changing the voltage and clock improperly may damage the device.)

### **Notice**

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



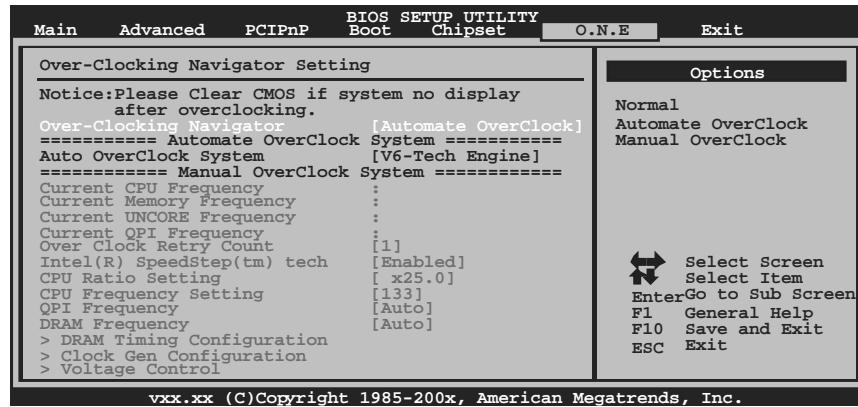
### **OverClock Navigator**

OverClock .Navigator is designed for beginners in overclock field.  
Based on many test and experiments from Biostar Engineer Team, OverClock Navigator provides 3 default overclock configurations that are able to raise the system performance.  
Options: Normal (Default) / Automate OverClock / Manual OverClock

# TH55XE BIOS Manual

---

## Auto OverClock System



The Overclock Navigator provides 3 different engines helping you to overclock your system. These engines will boost your system performance to different level.

Options:

V6 Tech Engine

This engine will make a good over-clock performance.

V8 Tech Engine

This engine will make a better over-clock performance.

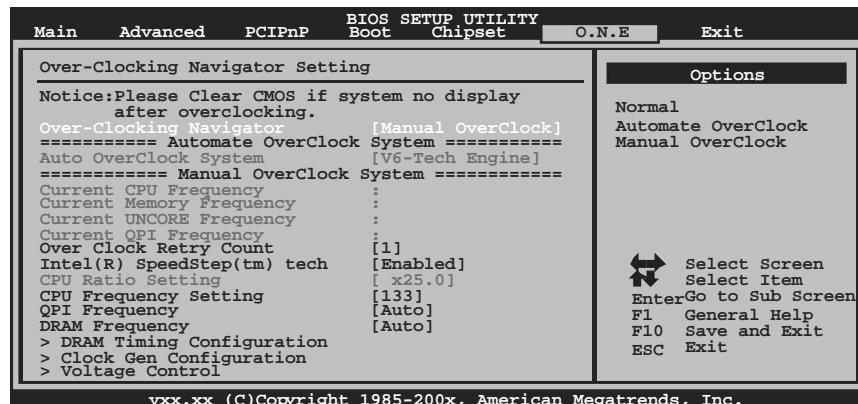
V12 Tech Engine

This engine will make a best over-clock performance.

# **TH55XE BIOS Manual**

---

## **Manual Overclock System (M.O.S.)**



MOS is designed for experienced overclock users.  
It allows users to customize personal overclock setting.

### **Over Clock Retry Count**

This item allows you to set overclock fail retry times.  
Options: 1 (Default) / 2 / 3

### **Intel(R) SpeedStep(tm) Tech**

This item allows you to enable SpeedStep technology for better power saving.  
SpeedStep is a technology built into some Intel processors that allows the clock speed of the processor to be dynamically changed by software.  
Options: Enabled (Default) / Disabled

### **CPU Ratio Setting**

This item allows you to set the CPU ratio frequency. This item is adjustable only when SpeedStep Tech is set to Disabled.  
Options: x25.0 (Default) / x11.0 ~ x26.0

### **CPU Frequency Setting**

This item allows you to select the CPU Frequency.  
Options: Min= 100MHz; Max= 800MHz

# **TH55XE BIOS Manual**

---

## **QPI Frequency**

This item allows you to control the QPI Clock.

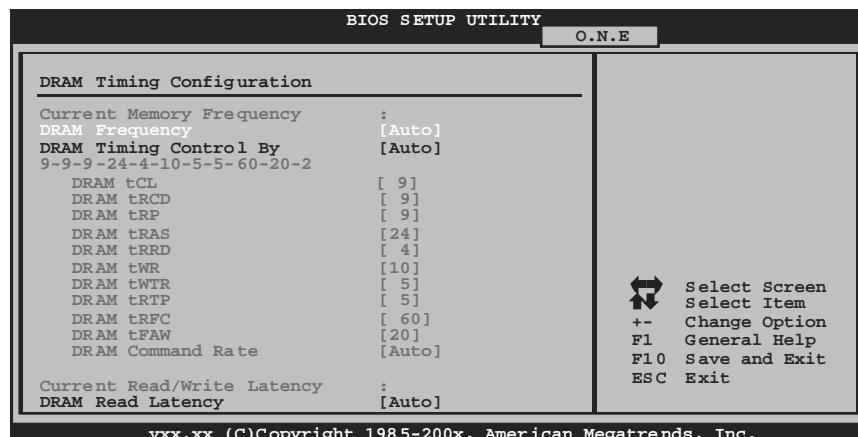
Options: Auto (Default) / 1600MHz / 1867MHz / 2133MHz / 2400MHz / 2666MHz / 2933MHz / 3200MHz

## **DRAM Frequency**

This item allows you to control the Memory Clock.

Options: Auto (Default) / 800MHz / 1067MHz / 1333MHz

## **DRAM Timing Configuration**



## **DRAM Frequency**

This item allows you to control the Memory Clock.

Options: Auto (Default) / 800MHz / 1067MHz / 1333MHz

## **DRAM Timing Control By**

This item allows you to choose to manually or automatically regulate the DRAM Timing.

Options: Auto (Default) / Manual

# ***TH55XE BIOS Manual***

---

---

## **DRAM tCL**

Options: 9 (Default) / 3 ~ 15

## **DRAM tRCD**

Options: 9 (Default) / 3 ~ 15

## **DRAM tRP**

Options: 9 (Default) / 3 ~ 15

## **DRAM tRAS**

Options: 24 (Default) / 9 ~ 63

## **DRAM tRRD**

Options: 4 (Default) / 5 ~ 15

## **DRAM tWR**

Options: 10 (Default) / 3 ~ 31

## **DRAM tWTR**

Options: 5 (Default) / 4 ~ 31

## **DRAM tRTP**

Options: 5 (Default) / 4 ~ 15

## **DRAM tRFC**

Options: 60 (Default) / 15 ~ 255

## **DRAM tFAW**

Options: 20 (Default) / 15 ~ 63

# **TH55XE BIOS Manual**

---

## **DRAM Command Rate**

This item allows you to select command rate of DDR3.  
Options: Auto (Default) / 1T / 2T

## **DRAM Read/Write Latency**

Options: Auto (Default) / 1 ~ 32

## **Clock Gen Configuration**



### **PCIE Clock By**

This item allows you to determine how to set PCIE Clock, by auto or manual.  
Options: Auto (Default) / Manual

### **PCIE Frequency Setting**

This item allows you to select the PCIE clock control.  
Options: 100 (Default) / Min=100; Max=150

### **CPU Clock delay**

This item allows you to select CPU Clock delay.  
Options: No delay (Default) / 50ps / 100ps / 150ps / 200ps / 250ps / 300ps / 350ps  
/ 400ps / 450ps / 500ps

# **TH55XE BIOS Manual**

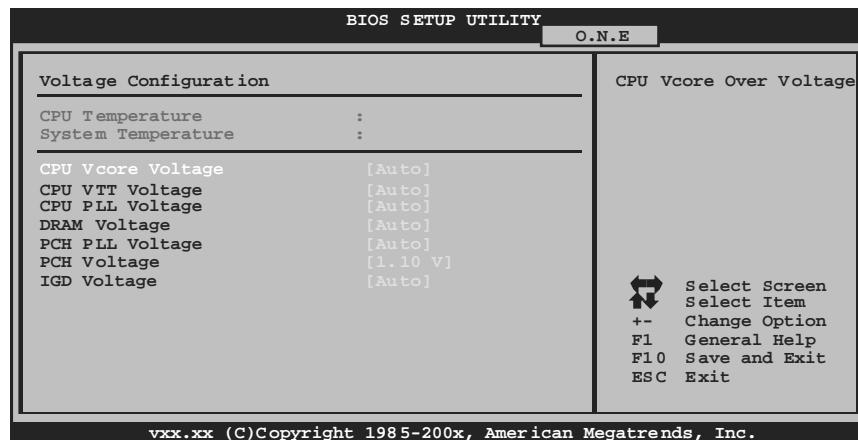
---

## **(G)MCH Clock delay**

This item allows you to select MCH Clock delay.

Options: No delay (Default) / 50ps / 100ps / 150ps / 200ps / 250ps / 300ps / 350ps  
/ 400ps / 450ps / 500ps

## **Voltage Control**



### **CPU Vcore Voltage**

This item allows you to select CPU Vcore Voltage control.

Options: Auto (Default) / SPEC / -0.080V ~ +1.260V (Interval: 0.02V)

### **CPU VTT Voltage**

This item allows you to select CPU VTT Voltage control.

Options: Auto (Default) / 1.150V ~ +2080V (Interval: 0.015V)

### **CPU PLL Voltage**

This item allows you to select CPU PLL Voltage control.

Options: Auto (Default) / 1.800V ~ 2.730V (Interval: 0.015V)

### **DRAM Voltage**

This item allows you to select DRAM Voltage control.

Options: Auto (Default) / 1.600V ~ 2.530V (Interval: 0.015V)

# **TH55XE BIOS Manual**

---

---

## **PCH PLL Voltage**

This item allows you to select PCH PLL Voltage control.  
Options: Auto (Default) / 1.100V ~ 2.030V (Interval: 0.015V)

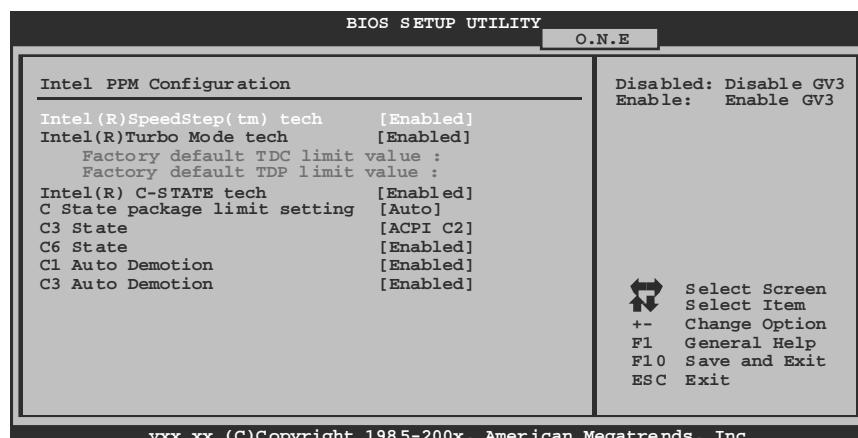
## **PCH Voltage**

This item allows you to select PCH Voltage control.  
Options: 1.10 V (Default) / 1.15 V / 1.20 V / 1.25 V

## **IGD Voltage**

This item allows you to select Onboard Voltage control.  
Options: Auto (Default) / 1.18V ~ 1.78V (Interval: 0.02V)

## **Intel PPM Configuration**



### **Intel(R) SpeedStep(tm) tech**

This item allows you to enable SpeedStep technology for better power saving. SpeedStep is a technology built into some Intel processors that allows the clock speed of the processor to be dynamically changed by software.  
Options: Enabled (Default) / Disabled

# **TH55XE BIOS Manual**

---

---

## **Intel(R) Turbo Mode tech**

Turbo mode allows processor cores to run faster than marked frequency in specific condition.

Options: Enabled (Default) / Disabled

## **Intel(R) C-STATE Tech**

This item allows you to control the C-State power management functions of the processor. (CState: CPU idle is set to C2/C3/C4)

Options: Enabled (Default) / Disabled

## **C State package limit setting**

The selected option will program into C State package limit register.

Options: Auto (Default) / C1 / C3 / C6 / C7

## **C3 State**

This item allows you to select Nehalem C state action.

Options: ACPI C2 (Default) / ACPI C3 / Disabled

## **C6 State**

This item allows you to select Nehalem C state action.

Options: Enabled (Default) / Disabled

## **C1 Auto Demotion**

When enabled, CPU will conditionally demote C3/C6/C7 requests to C1 based on uncore auto-demote information.

Options: Enabled (Default) / Disabled

## **C3 Auto Demotion**

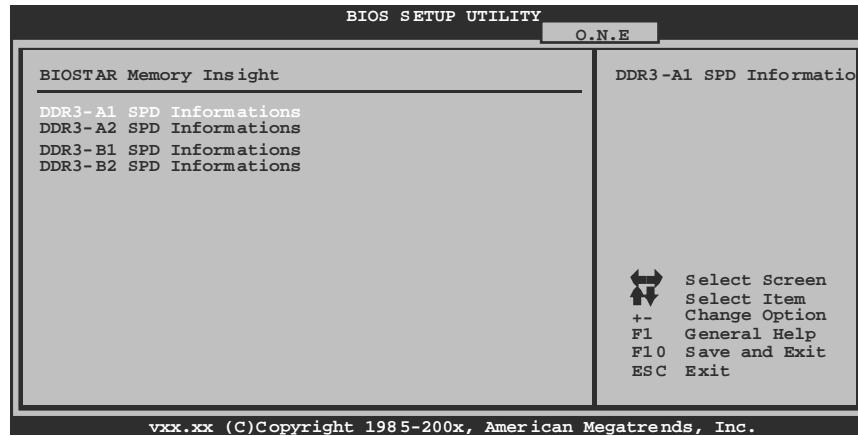
When enabled, CPU will conditionally demote C6/C7 requests to C3 based on uncore auto-demote information.

Options: Enabled (Default) / Disabled

# **TH55XE BIOS Manual**

---

## **BIOSTAR Memory Insight**



## **DDR3-A1/B1/A2/B2 SPD Informations**

These items display SPD information of DDR3 memory.

## **G.P.U Phase Control**



## **G.P.U Phase Mode**

This item allows you to control G.P.U Phase Mode (power saving technology).  
Options: Auto (Default) / Off / PSI On

# **TH55XE BIOS Manual**

---

## **CPU Load Line**

This item allows you to control CPU Load Line.  
Options: Enabled (Default) / Disabled

## **PWM Frequency**

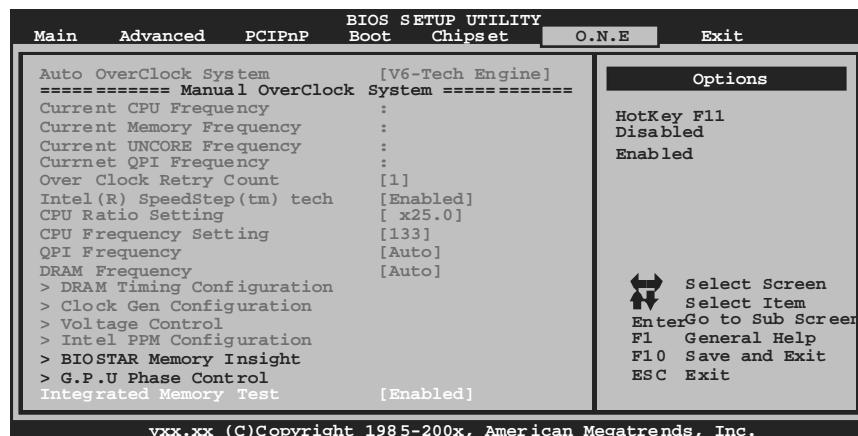
This item allows you to Select PWM Frequency.  
Options: 200 KHz (Default) / 180 KHz / 220 KHz / 240 KHz

## **Integrated Memory Test**

Integrated Memory Test allows users to test memory module compatibilities without additional device or software.

### **Step 1:**

This item is HotKey F11 on default; change it to “Enable” to precede memory test.

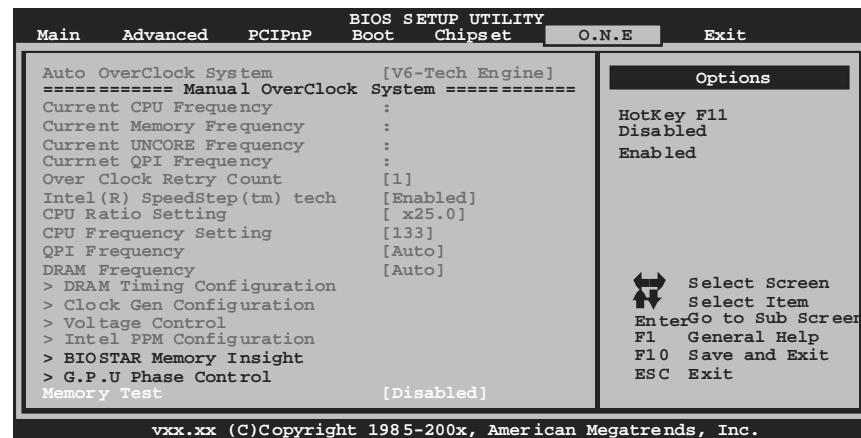


# **TH55XE BIOS Manual**

---

## **Step 2:**

When the process is done, change the setting back from “Enabled” to “HotKey F11” or “Disabled” to complete the test.



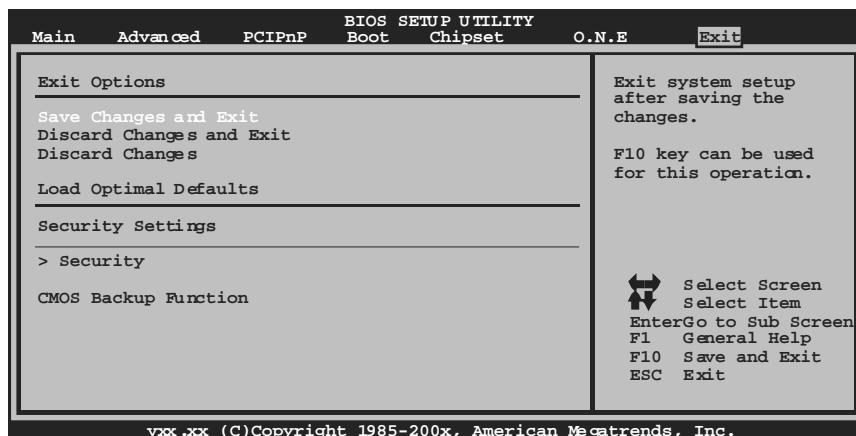
# ***TH55XE BIOS Manual***

---

---

## **7 Exit Menu**

This menu allows you to load the optimal default settings, and save or discard the changes to the BIOS items.



### **Save Changes and Exit**

Save all configuration changes to CMOS RAM and exit setup.

### **Discard Changes and Exit**

Abandon all changes made during the current session and exit setup.

### **Discard Changes**

Abandon all changes made during the current session and restore the previously saved values.

### **Load Optimal Defaults**

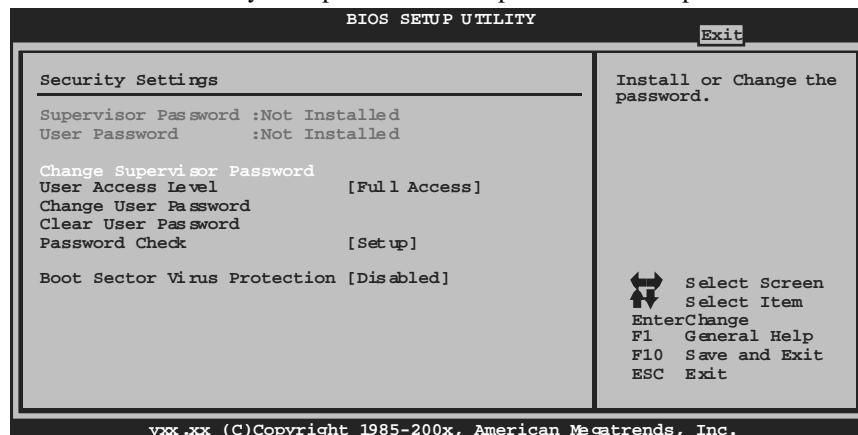
This selection allows you to reload the BIOS when problem occurs during system booting sequence. These configurations are factory settings optimized for this system.

# **TH55XE BIOS Manual**

---

## **Security**

This sub-menu allows you to provide/revise supervisor and user password.



### **Change Supervisor Password**

Setting the supervisor password will prohibit everyone except the supervisor from making changes using the CMOS Setup Utility. You will be prompted with to enter a password.

### **User Acess Level**

This item allows supervisor to set the user level.

Options: Full Access (Default) / No Access / View Only / Limited

### **Change User Password**

If the Supervisor Password is not set, then the User Password will function in the same way as the Supervisor Password. If the Supervisor Password is set and the User Password is set, the "User" will only be able to view configurations but will not be able to change them.

### **Clear User Passw ord**

This item is for clearing user password.

# **TH55XE BIOS Manual**

---

## **Password Check**

This item is for setting the timing that checking password.  
Options: Setup (Default) / Always

## **Boot Sector Virus Protection**

This option allows you to choose the VIRUS Warning feature that is used to protect the IDE Hard Disk boot sector. If this function is enabled and an attempt is made to write to the boot sector, BIOS will display a warning message on the screen and sound an alarm beep.

Options: Disabled (Default) / Enabled

## **CMOS Backup Function**

It allows users to save different CMOS settings into BIOS-ROM and reload any saved CMOS setting for customizing system configurations.

Moreover, users are able to save an ideal overclock setting during overclock operation.

There are 10 sets of record addresses in total, and users are able to name the CMOS data according to personal preference.

